

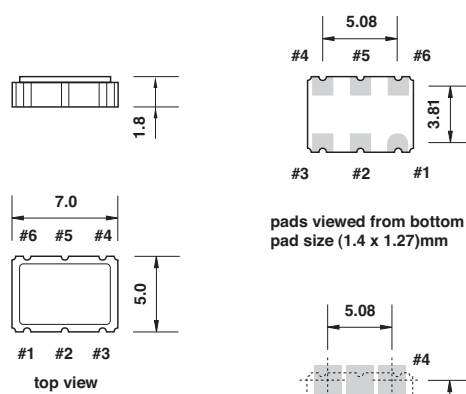
**Type VT - L,V smd VCXO**  
**(1.5 ~ 200)MHz,**  
**output PECL or LVDS**  
**(7.0 x 5.0)mm, height 1.8mm**

A high frequency, smd, voltage controlled crystal oscillator manufactured over the frequency range of 1.5MHz to 200MHz. Good linearity, wide pulling range, +3.3V d.c. and 2.5Vd.c. supply.

A standard package providing an excellent combination of parameters within a small smd enclosure.

Supplied on tape and reel with 1000 and 3000 pieces per reel.

**Dimensions(mm)**



pad connections:  
 #1 voltage control  
 #2 tri-state  
 #3 ground  
 #4 output  
 #5 complimentary output  
 #6  $V_{DD}$

output inhibit:  
 #2 high: output oscillation  
 #2 low: output high impedance

pads viewed from bottom  
 pad size (1.4 x 1.27)mm

suggested land pattern  
 pad size (2.0 x 1.8)mm

connect 0.1 $\mu$ F capacitor  
 between  $V_{DD}$  and ground

**Frequency stability -vs- temperature:**

TEMP. RANGE	COMBINED TOLERANCE	
(-10 +60) $^{\circ}$ C	$\pm 25$ ppm	$\pm 50$ ppm
(-20 +70) $^{\circ}$ C	$\pm 25$ ppm	$\pm 50$ ppm
(-40 +85) $^{\circ}$ C		$\pm 50$ ppm

Tolerance inclusive of calibration tolerance at +25 $^{\circ}$ C, temperature tolerance, load variation and supply voltage variation, first year ageing, vibration and shock

**Electrical specification:**

	PECL				LVDS				
	3.3Vd.c.		2.5Vd.c.		3.3Vd.c.		2.5Vd.c.		
	min.	max.	min.	max.	min.	max.	min.	max.	
supply voltage $V_{DD} \pm 5\%$	3.135	3.465	2.375	2.625	3.135	3.465	2.375	2.625	Vd.c.
frequency range	1.5	200	65	200	1.5	200	65	200	MHz
pulling range	$\pm 50$								ppm
control voltage range	0.3	3.0	0	2.5	0.3	3.0	0	2.5	V
supply current (1.5 ~ 65)MHz	-	75	-	75	-	45	-	45	mA
supply current (65 ~ 200)MHz	-	100	-	100	-	80	-	80	mA
o/p high	2.275	-	1.475	-	-	1.6	-	1.6	V
o/p low	-	1.68	-	1.095	0.9	-	0.9	-	V
rise and fall time, $t_r$	-	1.0	-	1.0	-	1.0	-	1.0	nano sec.
start up time	-	3	-	3	-	3	-	3	milli sec.
tri-state: active o/p	-	0.3V <sub>DD</sub>	-	0.3V <sub>DD</sub>	-	0.3V <sub>DD</sub>	-	0.3V <sub>DD</sub>	V
tri-state: high impedance o/p	0.7V <sub>DD</sub>	-	0.7V <sub>DD</sub>	-	0.7V <sub>DD</sub>	-	0.7V <sub>DD</sub>	-	V
linearity	-	10	-	10	-	10	-	10	%
modulation bandwidth	25	-	25	-	25	-	25	-	kHz
input impedance	50	-	50	-	50	-	50	-	K $\Omega$
RMS phase jitter(integrated 12kHz ~ 20MHz)									
$f_0 < 100$ MHz	-	1.0	-	1.0	-	1.0	-	1.0	pico.sec
100MHz < $f_0$ < 125MHz	-	0.7	-	0.7	-	0.7	-	0.7	
125MHz < $f_0$ < 150MHz	-	0.5	-	0.5	-	0.5	-	0.5	
150MHz < $f_0$	-	0.3	-	0.3	-	0.3	-	0.3	
ageing	-	$\pm 3$	-	$\pm 3$	-	$\pm 3$	-	$\pm 3$	ppm
storage temperature range	(-55 +125) $^{\circ}$ C								$^{\circ}$ C

**Ordering information**

<b>EXAMPLE</b>	<i>type VT - L smd VCXO, 155.52MHz, ±50ppm pulling, +3.3Vd.c., ±25ppm(-10 +60)°C, output PECL</i>
<b>TFC PART NUMBER</b>	<b>VT - L 155.52M E M I</b>
<b>VT - L</b>	<i>type: VT = VCXO type VT; (7.0 x 5.0)mm package, L; PECL output, V; LVDS output</i>
<b>155.52</b>	<i>frequency: 155.52MHz, frequency range (1.5 ~ 200)MHz</i>
<b>E</b>	<i>supply voltage: E = +3.3Vd.c.</i>
<b>M</b>	<i>frequency stability: M = ±25ppm</i>
<b>I</b>	<i>temperature range: I = (-10 +60)°C</i>
<b>OPTIONS</b>	
<b>supply voltage</b>	<i>E: +3.3Vd.c., J: +2.5Vd.c.</i>
<b>output</b>	<i>L: PECL, V: LVDS</i>
<b>frequency stability</b>	<i>M: ±25ppm, P: ±50ppm</i>
<b>temperature range</b>	<i>I: (-10 +60)°C, C: (-20 +70)°C, L: (-40 +85)°C</i>