

Type OWM smd clock oscillator multiplier frequency, fast delivery, (8 ~ 1500)MHz, output LVPECL or LVDS, low jitter, (5.0 x 3.2)mm, height 1.30mm,

Fast delivery XO for prototyping and production quantities.

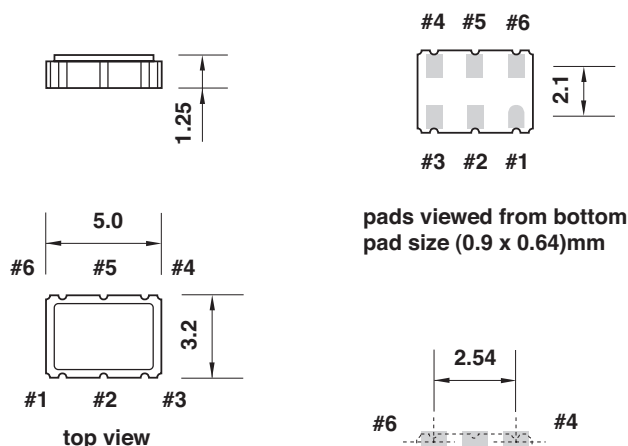
A very high frequency, smd clock oscillator available to any frequency over the range of 8MHz ~ 1500MHz.

Low jitter, 3rd overtone crystal design, +3.3V d.c. and 2.5Vd.c. supply.

An industry standard ceramic (5.0 x 3.2)mm package providing an excellent combination of parameters within a small smd enclosure.

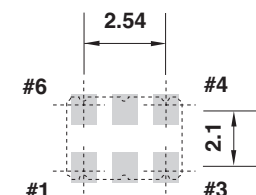
Available on tape and reel with 1000 and 3000 pieces per reel.

Dimensions(mm)



- pad connections:
- #1 tri-state/ NC
 - #2 NC/ tri-state
 - #3 ground
 - #4 output
 - #5 complimentary output
 - #6 V_{DD}

pads viewed from bottom pad size (0.9 x 0.64)mm



suggested land pattern pad size (1.2 x 0.85)mm

connect 0.1µF capacitor between V_{DD} and ground - pads #6 and #3

Frequency stability -vs- temperature:

TEMP. RANGE	COMBINED TOLERANCE	
(-10 +60)°C	±25ppm	±50ppm
(-20 +70)°C	±25ppm	±50ppm
(-40 +85)°C	conditional	±50ppm

Tolerance inclusive of calibration tolerance at +25°C, temperature tolerance, load variation and supply voltage variation, first year ageing, vibration and shock

Electrical specification:

	LVPECL				LVDS				
	3.3Vd.c.		2.5Vd.c.		3.3Vd.c.		2.5Vd.c.		
	min.	max.	min.	max.	min.	max.	min.	max.	
supply voltage V _{DD} ±5%	3.135	3.465	2.375	2.625	3.135	3.465	2.375	2.625	Vd.c.
frequency range	(8 ~ 1500)MHz								MHz
standard frequencies	106.25, 125, 133.33, 150.00, 155.52, 156.25, 187.5, 212.5, 312.5, 622.08								MHz
supply current (80 ~ 1500)MHz	-	50	-	50	-	50	-	50	mA
o/p high (logic 1)	2.275	-	1.475	-	-	1.6	-	1.6	V
o/p low (logic 0)	-	1.68	-	0.88	0.9	-	0.9	-	V
rise and fall time, t _r	-	1.0	-	1.0	-	1.0	-	1.0	nano sec.
start up time	-	10	-	10	-	10	-	10	milli sec.
tri-state input to pin #1 or #2: active o/p	2.31	-	1.75	-	2.31	-	1.75	-	V
tri-state input to pin #1 or #2: high impedance o/p	-	0.99	-	0.75	-	0.99	-	0.75	V
RMS phase jitter(integrated 12kHz ~ 20MHz)	1.0		1.0		1.0		1.0		pico.sec
Phase noise @156.25MHz									
+100 Hz	-	-85	-	-85	-	-85	-	-85	dBc/Hz
+1 kHz	-	-105	-	-105	-	-105	-	-105	
+10 kHz	-	-115	-	-115	-	-115	-	-115	
ageing	-	±3	-	±3	-	±3	-	±3	ppm
storage temperature range	(-55 +125)°C								°C

Ordering information

EXAMPLE	<i>type OWM smd clock oscillator, 155.52MHz, +3.3Vd.c. supply, inhibit on pin #2, ±50ppm(-10 +60)°C, output LVPECL</i>
TFC PART NUMBER	OWM 155.52M E M G I L
OWM	<i>type: OWM = clock oscillator type OWM, smd, (5.0 x 3.2)mm</i>
155.52	<i>frequency: 155.52MHz, frequency range (8 ~ 1500)MHz</i>
E	<i>supply voltage: E = +3.3Vd.c.</i>
M	<i>inhibit pin #2: M = inhibit on pin #2</i>
G	<i>frequency stability: G = ±50ppm</i>
I	<i>temperature range: I = (-10 +60)°C</i>
L	<i>output: L = LVPECL</i>
OPTIONS	
supply voltage	<i>E: +3.3Vd.c., J: +2.5Vd.c.</i>
tri-state inhibit pin	<i>M: inhibit on pin #2, N: inhibit on pin #1</i>
frequency stability	<i>D: ±25ppm, G: ±50ppm</i>
temperature range	<i>I: (-10 +60)°C, C: (-20 +70)°C, L: (-40 +85)°C</i>
output	<i>L: LVPECL, V: LVDS</i>